



FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS  
FOR APPLICANT'S INFORMATION  
DISCLOSURE STATEMENT**

ATTY. DOCKET NO.  
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SERIAL NO.: 09/757,965

APPLICANT:  
T. H. NingFILING DATE:  
January 10, 2001

GROUP: 2811

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
	AC						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AD							
	AE							
	AF							

## OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

TT	AG	Y. Taur and T.H. Ning, Fundamentals of Modern VLSI Devices, Chapter 6, Bipolar Devices, Cambridge University Press, 1998, pp. 292-347.
TT	AH	T. Hiramoto et al., IEDM Tech. Digest, pp. 39-42, 1992.
TT	AI	K. Washio et al., IEDM, p. 741, 2000.
	AJ	

EXAMINER

*Thien Ning*

DATE CONSIDERED

11-11-02

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.